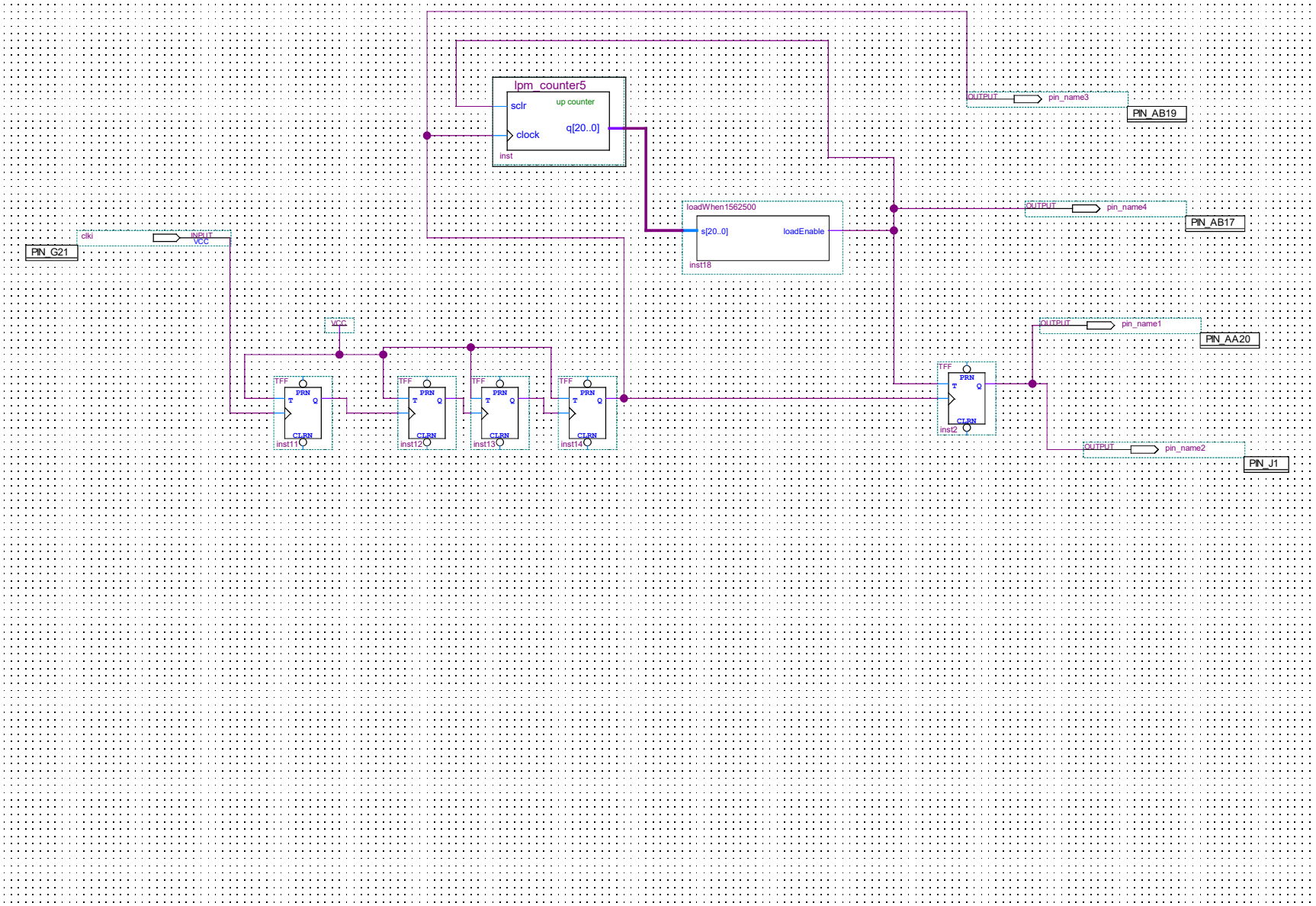
lpm_counter5
sclr
up counter
clock
q[20..0]
inst
loadWhen1562500
s[20..0]
loadEnable
inst18
OUTPUT
pin_name3
PIN_AB19
OUTPUT
pin_name4
PIN_AB17
clk
INPUT
VCC
PIN_G21
VCC
TFF
PRN
T
Q
CLRN
inst11
inst12
inst13
inst14
inst2
OUTPUT
pin_name1
PIN_AA20
OUTPUT
pin_name2
PIN_J1